

PTO/SB/21 (09-04)

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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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**TRANSMITTAL  
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

9

Application Number

10/815,453

Filing Date

March 31, 2004

First Named Inventor

Ricardo E. Gonzalez

Art Unit

2183

Examiner Name

Unknown

Attorney Docket Number

PA2718US

**ENCLOSURES (Check all that apply)**☐

Fee Transmittal Form

☐

Fee Attached

☐

Amendment/Reply

☐

After Final

☐

Affidavits/declaration(s)

☐

Extension of Time Request

☐

Express Abandonment Request

☒

Information Disclosure Statement

☐

Certified Copy of Priority Document(s)

☐Reply to Missing Parts/  
Incomplete Application☐Reply to Missing Parts  
under 37 CFR 1.52 or 1.53☐

Drawing(s)

☐

Licensing-related Papers

☐

Petition

☐Petition to Convert to a  
Provisional Application☐

Power of Attorney, Revocation

☐

Change of Correspondence Address

☐

Terminal Disclaimer

☐

Request for Refund

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CD, Number of CD(s) \_\_\_\_\_

☐ Landscape Table on CD

Remarks

Total page number does not include postcard or copies of cited references.

☐

After Allowance Communication to TC

☐Appeal Communication to Board  
of Appeals and Interferences☐Appeal Communication to TC  
(Appeal Notice, Brief, Reply Brief)☐

Proprietary Information

☐

Status Letter

☒Other Enclosure(s) (please identify  
below):

- 1) Confirmation Postcard;
- 2) PTO/SB/08 (x2);
- 3) Duplicate of IDS;
- 4) List of Co-Pending Applications;
- 5) Copies of 5 Cited References

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name

Carr &amp; Ferrell LLP

Cust. No. 22830

Signature

Printed name

Eugene G. Kim

Date

April 26, 2005

Reg. No.

46,267

**CERTIFICATE OF TRANSMISSION/MAILING**

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature

Typed or printed name

Eugene G. Kim, Reg. No. 46,267

Date

April 26, 2005

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):               Ricardo E. Gonzalez et al.  
APPLICATION NO.:           10/815,453  
FILED:                       March 31, 2004  
TITLE:                       Configuring a Multi-Processor System  
EXAMINER:                  Unknown  
GROUP ART UNIT:           2183  
ATTY.DKT.NO.:              PA2718US

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ALEXANDRIA, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**  
**Under 37 C.F.R. §§ 1.56, and 1.97-1.98**

SIR:


Pursuant to the provisions of 37 C.F.R. §§ 1.56 and 1.97-98 of the Rules of Practice in Patent Cases, enclosed herewith is form PTO-SB-08 listing several references and a list of co-pending applications; copies of the non-patent references are enclosed. The Examiner is requested to make these references of official record in the application. The references cited may be material to examination of the application and are submitted in compliance with Applicant(s)'s duty of disclosure as defined by 37 C.F.R. § 1.56.

No representation is made or intended as to the completeness of this list, nor is the inclusion of any reference on this list an admission that it is prior art or pertinent to this application.

Applicants believe no fee is due with this submission. If a fee is due, however, the Commissioner is hereby authorized to charge any necessary fee to Account Number 06-0600. A copy of this statement is submitted in duplicate for this purpose.

Respectfully submitted,  
Ricardo E. Gonzalez et al.

Dated: April 26, 2005

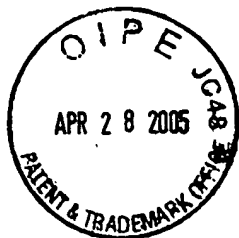
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Dated: April 26, 2005

By:   
Eugene G. Kim, Reg. No. 46,267



IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NO.:           10/815,453

FILED:                       March 31, 2004

TITLE:                       Configuring a Multi-Processor System

EXAMINER:                  Unknown

GROUP ART UNIT:           2183

ATTY.DKT.NO.:              PA2718US

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**List of Co-Pending Patent Applications That May Be Directed  
Towards Similar Subject Matter**

Examiner's Initials	Serial Number	First-Named Inventor	Title	Filing Date
	10/750,714	Ricardo E. Gonzalez	Systems and Methods for Software Extensible Multi-Processing	12/31/2003
	11/021,247	Ricardo E. Gonzalez	Systems and Methods for Selecting Input/Output Configuration in an Integrated Circuit	12/21/2004
Examiner's Signature:				Date



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UNITED STATES PATENT AND TRADEMARK OFFICE

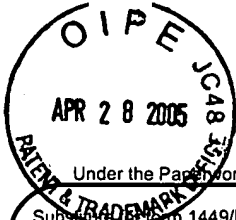
APPLICANT(S): Ricardo E. Gonzalez et al.  
APPLICATION NO.: 10/815,453  
FILED: March 31, 2004  
TITLE: Configuring a Multi-Processor System  
EXAMINER: Unknown  
GROUP ART UNIT: 2183  
ATTY.DKT.NO.: PA2718US

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Examiner's Signature:				Date



PTO/SB/08B (08-03)

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Subsection 1.01(a) 1449/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/815,453
Filing Date	March 31, 2004
First Named Inventor	Ricardo E. Gonzalez
Art Unit	2183
Examiner Name	Unknown
Attorney Docket Number	PA2718US

Sheet 1 of 1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		GOLDBLATT, K., "The Low-Cost, Efficient Serial Configuration of Spartan FPGAs," XAPPO98, November 13, 1998 (Version 1.0), XILINX.	
		SCOTT, S. et al., "The Cray T3E Network: Adaptive Routing in a High Performance 3D Torus," HOT Interconnects IV, August 15-16, 1996, Stanford University.	
		GOLESTARI, S.J., "A Stop-and-Go Queuing Framework for Congestion Management," Applications, Technologies, Architectures, and Protocols for Computer Communication, Proceedings of the ACM Symposium on Communications Architectures & Protocols, 1990, ACM Press, New York, NY USA.	
		DALLY, W. et al., "Deadlock Free Message Routing in Multiprocessor Interconnection Networks," Computer Science Department, California Institute of Technology, May 10, 1985.	
		GARLAND, D. et al., "An Introduction to Software Architecture," Advances in Software Engineering and Knowledge Engineering, 1993, Vol. 1, World Scientific Publishing Company, New Jersey, USA.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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